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PATENT  
Docket No. SPLX.P0100

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Mani Adeli

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In the application of:

Steven Teig

Serial No.: 10/062,993

Patent No.: 6,854,098

Filing Date: 01/31/2002

For: METHOD AND APPARATUS FOR  
PERFORMING TECHNOLOGY MAPPING

Examiner: Phallaka Kik

Group Art: 2825

TRANSMITTAL LETTER FOR REQUEST FOR CERTIFICATE OF CORRECTION

Attention Certificate of Corrections Branch  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Dear Sir:

Attached, please, find a Request for Certificate of Correction on PTO/SB/44 Form. There is no fee associated with this Request, as the corrected errors are not the fault of the Applicant. However, in the unlikely event that the Patent Office determines that additional fee is required, Applicant authorizes the Assistant Commissioner to charge the cost of such fees to Deposit Account No. 50-1128 referencing SPLX.P0100.

Dated: April 7, 2005

Respectfully submitted,

By: 

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**UNITED STATES PATENT AND TRADEMARK OFFICE  
CERTIFICATE OF CORRECTION**

PATENT NO.: 6,854,098 B2

DATED : February 08, 2005

INVENTOR(S) : Teig et al.

It is certified that errors appear in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

In the claims, column 45, line 31 after "identifying at least one replacement sub-network from a storage structure that stores replacement sub-networks that are bounded to the technology," should read --identifying at least one replacement sub-network from a storage structure that stores replacement sub-networks that are bound to the technology,--

In the claims, column 46, line 9, after "wherein during an iteration, a first candidate sub-network is replace in the design with a first replacement sub-network, and during another iteration, the second candidate sub-network being comprised of at least one but not all circuit elements of the first replacement sub-network." should read --wherein during an iteration, a first candidate sub-network is replaced in the design with a first replacement sub-network, and during another iteration, a second candidate sub-network is replaced by a second replacement sub-network, the second candidate sub-network being comprised of at least one but not all circuit elements of the first replacement sub-network.--

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PATENT NO. 6,854,098 B2

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